



#15/Amtd
Customer No. 22,852
Attorney Docket No. 04329.2613
5/17/03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Katsuhiko HIEDA

Application No.: 09/916,509

Filed: July 30, 2001

For: FIELD EFFECT TRANSISTOR
USING SIDE-WALL GATES TO
PREVENT PUNCH-THROUGH
(As Amended)

Group Art Unit: T. Le

Examiner: 2814

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

AMENDMENT

In reply to the Office Action dated March 5, 2003, please amend the application as follows:

IN THE CLAIMS:

Please cancel claim 23 without prejudice or disclaimer of the subject matter thereof.

REMARKS

In the Office Action, the Examiner objected to claim 23 under 35 C.F.R. 1.75(c) as being of improper dependent form; rejected claims 1, 2, 23, 35-37, and 39-45 under 35 U.S.C. § 102(b) as anticipated by Miyawaki et al., U.S. Patent No. 5,567,962

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